AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the application.

LISTING OF CLAIMS

1.   (Previously Presented) A method for dynamic allocation of slot bandwidth on a switch, comprising:

providing $B/\Delta B$ pieces of N-selected-one devices, input bandwidth of every N-selected-one device being $N*\Delta B$, N denoting the number of slots for dynamic bandwidth allocation, B denoting bandwidth need to be dynamically allocated, ΔB denoting a minimum allocated bandwidth unit;

communicating each slot with an input of each N-selected-one device, and communicating outputs of the N-selected-one devices with a main switch module;

controlling the N-selected-one devices to allocate the bandwidth to communicated slots.

2.   (Previously Presented)   The method according to Claim 1, further comprising:

controlling, by the main switch module, a programmable logic chip to output strobe signals, and

wherein the controlling the N-selected-one devices to allocate the bandwidth to communicated slots comprises controlling the N-selected-one devices by the programmable logic chip through the strobe signals.

3.   (Previously Presented) The method according to Claim 1, wherein the programmable logic chip is an Electrically Programmable Logical Device (EPLD) with type EPM7256AEQC208-10.

4.   (Original) The method according to Claim 1, wherein the N-selected-one device is a two-selected-one device.

5.   (Original) The method according to Claim 4, wherein the two-selected-one device is a 1.25GHz Ethernet signal driver with type VSC7132YB.

6.   (Previously Presented)   An apparatus for dynamic allocation of slot bandwidth, comprising:

N slots, N denoting the number of slots for dynamic bandwidth allocation;

$B/\Delta B$ pieces of N-selected-one devices, input bandwidth of every N-selected-one device being $N*\Delta B$, B denoting bandwidth need to be dynamically allocated, ΔB denoting a minimum allocated bandwidth unit; and

a main switch module,

wherein N inputs of each N-selected-one device communicate with the N slots respectively, an output of each N-selected-one device communicates with the main switch module, and the main switch module communicates with the N-selected-one devices for controlling the N-selected-one devices to allocate the bandwidth to communicated slots.

7.   (Previously Presented)   The apparatus according to claim 6, further comprising:

a programmable logic chip controlled by the main switch module for providing strobe signals to control the N-selected-one devices.

8.   (Previously Presented) The apparatus according to claim 7, wherein the programmable logic chip is an Electrically Programmable Logical Device (EPLD).

9.   (Currently Amended)   An apparatus for dynamic allocation of slot bandwidth, comprising:

~~two~~ N slots, N being an integer greater than 1;

a main switch module;

B/ΔB pieces of ~~two~~N-selected-one devices that each have N inputs and an output and that each are operable to select one of the N inputs to establish communication between the selected input and the output, ~~input bandwidth of every two-selected-one device being 2*ΔB,~~ B denoting bandwidth ~~need~~ to be dynamically allocated, ΔB denoting a minimum allocated bandwidth unit and being no greater than a half of B; and

~~the main switch module,~~

wherein ~~two inputs of each two-selected-one device communicate with the two slots respectively, an output of each two-selected-one device communicates with the main switch module, and~~ the N inputs of each of the N-selected-one devices are in communication with the N slots respectively, the output of each of the N-selected-one devices is in communication with the main switch module, the main switch module ~~communicates with~~controls each of the N-selected-one devices to select one of the N slots and to allocate one ΔB bandwidth to the selected slot. ~~the two-selected-one devices fro controlling the two-selected-one devices to allocate the bandwidth to communicated slots.~~